University of Mumbai



1T00934

Winter 2025

EXAMINATION TIME TABLE

PROGRAMME - S.E. (Electronics & Computer Science) (Choice Based) (R-2020-21 'C' Scheme)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	40721	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	40722	Electronic Circuits
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	40723	Controls & Instrumentation
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	40724	Microprocessors & Microcontrollers
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Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	40725	Discrete Structures & Automata Theory

Important Note: • The candidates appearing for the examination should report 20 minutes before the start of examination.
• Mobile phones and other electronic gadgets are prohibited in the examination hall.

• Change if any, in the timetable shall be communicated on the University website.

Mumbai - 400 098 20th October, 2025.

Dr. Pooja Raundale Director Board of Examinations & Evaluation